Faculty introduction to **BSc students**
Faculty of Electrical Engineering and Informatics
Budapest University of Technology and Economics

Dr. Eszter Udvary
associate professor
BSc and MSc English program director
Highlights

1. Introduction to the faculty
2. Degree programs
3. Q&A
Faculties of the BME

- Faculty of Civil Engineering (1782)
- Faculty of Mechanical Engineering (1871)
- Faculty of Architecture (1873)
- Faculty of Chemical Engineering (1873)
- Faculty of Electrical Engineering and Informatics (1949)
- Faculty of Transportation Engineering (1951)
- Faculty of Natural Sciences (1987)
- Faculty of Economic and Social Sciences (1998)
Departments at the faculty

- Department of Automation and Applied Informatics
- Department of Broadband Infocommunications and Electromagnetic Theory
- Department of Computer Science and Information Theory
- Department of Control Engineering and Information Technology
- Department of Electric Power Engineering
- Department of Electron Devices
- Department of Electronics Technology
- Department of Measurement and Information Systems
- Department of Networked Systems and Services
- Department of Telecommunications and Media Informatics
Faculty buildings
Faculty buildings: I
Faculty buildings: Q
Faculty buildings: V1
Degree programs in English

- Electrical Engineering (BSc) – 7 semesters
- Computer Engineering (BSc) – 7 semesters
- Electrical Engineering (MSc) – 4 semesters
- Computer Engineering (MSc) – 4 semesters
- Electrical Engineering (PhD) – 8 semesters
- Computer Engineering (PhD) – 8 semesters
Curriculum - Electrical Engineering (BSc)

- 7 semesters, 210 credits (ECTS)
- Three study specialization blocks:
  - Embedded and Controller Systems
  - Infocommunication Systems
  - Sustainable Electric Energetics
Curriculum - Computer Engineering (BSc)

- 7 semesters, 210 credits (ECTS)
- Two study specialization blocks:
  - Infocommunications
  - Software Engineering
European Credit Transfer and Accumulation System (ECTS)

• It helps students to move between countries and to have their academic qualifications and study periods abroad recognised

• It enhances the flexibility of study programmes for students

• ECTS credits represent learning based on defined learning outcomes and their associated workload.

• 1 credit ≈ 30 workhours
Workload

• 1 semester = 20 weeks
  – 1 week registration period
  – 14 weeks study period
  – 1 week recap period
  – 4 weeks exam period
• 900 workhours / semester
  – 30 credits / semester
  – 30 workhours / credits
• 45 workhours / week (5 days)
  => **9 hours / day** => full time study
• 25-30 contact hours / week
# Subjects

<table>
<thead>
<tr>
<th>Subject Type</th>
<th>Mid-term Assessments</th>
<th>End of the study period</th>
<th>Exam period</th>
</tr>
</thead>
<tbody>
<tr>
<td>Exam</td>
<td>Mid-semester test&lt;br&gt;Mid-semester exam&lt;br&gt;Homework</td>
<td>Signature</td>
<td>Exam</td>
</tr>
<tr>
<td>Mid-semester mark</td>
<td>Mid-semester test&lt;br&gt;Mid-semester exam&lt;br&gt;Homework&lt;br&gt;Laboratory course</td>
<td>Grade (based on the mid-semester results)</td>
<td>-</td>
</tr>
</tbody>
</table>

- Subjects – with few exceptions – are only announced once a year, either in the spring or in the fall semester!
- Please handle the subject based on neptun ID (NOT title)
### Courses

<table>
<thead>
<tr>
<th>type</th>
<th>Attendance requirement</th>
<th>note</th>
</tr>
</thead>
<tbody>
<tr>
<td>Theory</td>
<td>Max. 70%</td>
<td>If it is in the subject description</td>
</tr>
<tr>
<td>Practice</td>
<td>70%</td>
<td></td>
</tr>
<tr>
<td>Laboratory</td>
<td>100%</td>
<td>Attendance is compulsory</td>
</tr>
<tr>
<td>exam</td>
<td>-</td>
<td>a course without contact hours</td>
</tr>
<tr>
<td></td>
<td></td>
<td>- comprehensive exam</td>
</tr>
<tr>
<td></td>
<td></td>
<td>- If you have signature, but you did not get grade =&gt; you have to repeat the examination in the next exam period</td>
</tr>
</tbody>
</table>

BME expects students to attend the lectures
Contacts

- For issues related to the administration of your studies you always have to turn Ms. Margit Nagy (Electrical Engineering) or Ms. Violetta Máté (Computer Engineering)
  - location: building R, ground floor
  - phone: +36-1- 463-1111 / ext. 4609
  - e-mail: nagy.margit@kth.bme.hu and mate.violetta@kth.bme.hu

- For issues related to your studies, scholarship, and personal life, you should turn to the international coordinator in the first place (e-mail: english_program_info@vik-dh.bme.hu)

- The program director of BSc and MSc studies is Ms. Eszter Udvary (e-mail: udvary@hvt.bme.hu)
Basic rule of administration

It is strictly forbidden to bypass the chain of hierarchy detailed above and to directly communicate to the rector/dean or any other university personnel without notifying the persons listed above. The violation of this rule will entail disciplinary measures !!!
When you contact anyone...

• Please explain
  – Your name and your Neptun ID
  – Your program (BSc/MSc/PhD, Electrical Engineering/Computer Engineering)
  – Your semester
  – Details of your problem
    • Who, what subject, when, why, what did happen...
  – Print screen (if you have problem with Neptun or other electronic system)
On-line study system in English

• you must use the electronic study system NEPTUN to handle all of your administration (https://frame.neptun.bme.hu/hallgatoi/login.aspx)
• All information can be found at the website vik.bme.hu/en
DEAN'S MESSAGE

The Faculty of Electrical Engineering and Informatics (VIK) of Budapest University of Technology and Economics (BME) carries on the traditions of the above 230 year old University. The proof thereof is the recognition of our degrees all around the world as well as the involvement and appreciation of our professors and researchers in the international scientific scene and organisations.

Almost all multinational electronics and IT corporations well-known in Asia have established R&D laboratories and centres attached to various departments of the faculty – Ericsson, Morgan Stanley, Nokia, Siemens, Samsung, Huawei, HP, IBM, just to name a few – where students can get hands-on information on the expectations of the partner companies.

The 2001 January issue of Nature published an article with the title "The 20th century was created in Budapest".

It shows that Budapest irrevocably became part of history that determined the advancement of natural sciences in the last century. Many of the illustrious scientists either studied or taught at the University.

This constitutes such a responsibility for current education that – inter alia – resulted in a prominent place in the Webometrics ranking of universities.

Ancient Romans had a pertinent term "genius loci", meaning the spirit of the place.

Join us to create the 21st century here, in the middle of Europe. Hungary. Budapest, a liveable and safe city with a colourful multicultural spirit, spicy dishes and Asian roots still retained in music providing an academic student environment and community. Budapest is a city with tons of culture events, from meeting international artists to supporting local talent.
For Current Students
B.S.C. PROGRAMS (CURRENT)

Electrical Engineering (program description, simplified roadmap, prerequisites)
Computer Engineering (program description, simplified roadmap, prerequisites)

Main training areas (210 credits)

<table>
<thead>
<tr>
<th>Area</th>
<th>Credits</th>
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</thead>
<tbody>
<tr>
<td>Natural science fundamentals</td>
<td>40-50</td>
</tr>
<tr>
<td>Economics and humanities</td>
<td>14-30</td>
</tr>
<tr>
<td>Professional core material</td>
<td>70-105</td>
</tr>
<tr>
<td>Specific professional knowledge</td>
<td>min. 40</td>
</tr>
<tr>
<td>Free electives</td>
<td>min. 10</td>
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</tbody>
</table>

BSc Electrical Engineering curriculum

1st semester

<table>
<thead>
<tr>
<th>Credits</th>
<th>Course code</th>
<th>Course name</th>
<th>Contact hours</th>
<th>Requirement</th>
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</thead>
<tbody>
<tr>
<td>6</td>
<td>BMETE90AX00</td>
<td>Mathematics A1</td>
<td>4 2</td>
<td>exam</td>
</tr>
<tr>
<td>4</td>
<td>BMETE11AX21</td>
<td>Physics 1</td>
<td>3 1</td>
<td>exam</td>
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<td>5</td>
<td>BMEVISZAA05</td>
<td>Foundation of computer science</td>
<td>2 2</td>
<td>exam</td>
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<tr>
<td>6</td>
<td>BMEVIII04A01</td>
<td>Digital design 1</td>
<td>3 1 1</td>
<td>exam</td>
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<tr>
<td>7</td>
<td>BMEVIII01A01</td>
<td>Basics of programming 1</td>
<td>2 2 2</td>
<td>mid-semester mark</td>
</tr>
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<td>3</td>
<td>BMET63EE11</td>
<td>English for Electrical Engineering and Informatics 1.</td>
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<td>mid-semester mark</td>
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2nd semester

<table>
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<tr>
<th>Credits</th>
<th>Course code</th>
<th>Course name</th>
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<th>Requirement</th>
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</thead>
<tbody>
<tr>
<td>6</td>
<td>BMETE90AX26</td>
<td>Mathematics A2</td>
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<td>mid-semester mark</td>
</tr>
<tr>
<td>4</td>
<td>BMETE11AX22</td>
<td>Physics 1</td>
<td>3 1</td>
<td>exam</td>
</tr>
<tr>
<td>6</td>
<td>BMEVIII04A00</td>
<td>Signals and systems 1</td>
<td>3 2</td>
<td>exam</td>
</tr>
<tr>
<td>5</td>
<td>BMEVIII04A02</td>
<td>Digital design 2</td>
<td>3 1</td>
<td>exam</td>
</tr>
<tr>
<td>6</td>
<td>BMEVIII01A01</td>
<td>Basics of programming 2</td>
<td>2 2</td>
<td>mid-semester mark</td>
</tr>
</tbody>
</table>
# B.S.C. Programs (Current)

## Electrical Engineering (program description, simplified roadmap, prerequisites)

### BSc degree program in Electrical Engineering

- **Semester structure**: registration (3w)
  - cases (14w): lectures, classroom practices, lab practices, quizzes, exams, homework assignments

- **RESMS (1w)**: mid-term reviews, late homework submission, early exams

**Thesis Defense**: organized during the last exam period in front of a committee. Includes presentation of thesis work, its discussion, and oral exam in one specialization subject. Written comprehensive final exam is required in advance.

**Thesis Work**
- Enrollment conditions: at least 174 credits are completed (up to 10 credits free elective)
- All courses of the first four semesters are completed
- All specialization courses are completed (up to the 6th semester)

**Credit Value**
- Each 15 credits represents 30 work hours

**Subject Legend**
- Fundamentals in natural sciences
- Economics and humanities
- Core engineering knowledge
- Free electives
- Pre-requisite for specialization studies

**Subject Types**
- Systems
- Embedded and control systems
- Electrical machines and applications
- Electrical power transmission and distribution
- Sustainable electric energies
- Communication systems
- Network and computer systems
- Mobile communication systems
- Space technology
- Radio systems and applications

**Electrical Power Transmission and Distribution**
- Electric power transmission and distribution
- Electrical machines and applications
- Electrical power transmission and distribution
- Sustainable electric energies

**Embedded and Control Systems**
- Embedded and control systems
- Industrial control systems
- Microcontroller and embedded systems
- Embedded operating systems and control
- Embedded and control systems lab.

**Communication Systems**
- Communication systems
- Network technologies
- Mobile communication systems
- Space technology
- Radio systems and applications lab.

**Sustainable Electric Energies**
- Sustainable electric energies
- Sustainable electric energies/laboratory

**Specialization Subjects**
- Embedded and control systems
- Electrical machines and applications
- Electrical power transmission and distribution
- Sustainable electric energies

**Subject Examples**
- High frequency system techniques
- Network technologies
- Mobile communication systems
- Space technology
- Radio systems and applications lab.

**Prerequisites**
- Fundamentals in natural sciences
- Economics and humanities
- Core engineering knowledge
- Free electives
- Pre-requisite for specialization studies

**BSc degree program roadmap. See www.vik.bme.hu/en for more details and regulations.**

*Last updated: 6 February, 2019*

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Electrical Engineering

Mandatory course list!
- TE90AX00 Mathematics A1 => „VIK” courses! **EN0-VIK and EN1-VIK**
- TE11AX21 Physics 1
- VISZAA05 Foundation of computer science
- VIIIAA04 Digital design 1
- **VIHIAA01** Basics of programming 1
- GT63EEI1 English for Electrical Engineering and Informatics 1 => you are already registered

Please register based on the neptun ID of the subject!

2020.09.03. BME Villamosmérnöki és Informatikai Kar
Mandatory course list!

- TE90AX21 Calculus 1 for informaticians
- TE11AX23 Physics 1i
- VISZAA03 Introduction to the theory of computing 1
- VIMIAA02 Digital design
- **VIEEAA00** Basics of programming 1
- GT63EEI1 English for Electrical Engineering and Informatics 1 => you are already registered

Please register based on the neptun ID of the subject!
Prerequisites: to make sure that each course provides you with knowledge which have sound foundations established by previous courses.
Prerequisites: to make sure that each course provides you with knowledge which have sound foundations established by previous courses.
BSc - Milestones

- Conditions for admission to the study specialization block
- Conditions for admission to the thesis defense session
Specializations

- Specializations start every fall semester
- The selection of specialization is always at the end of the 4th semester
- The number of students must exceed a certain threshold
- The decision on the type of specialization and the placement of students depends on the number and the results of applying students
- Not all specializations start every year
# Academic Calendar

## ACADEMIC CALENDAR for 2019/2020

**BME**  
Faculty of Electrical Engineering and Informatics (VIK)

### Autumn semester

<table>
<thead>
<tr>
<th>Event</th>
<th>Dates</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Registration period</strong></td>
<td>2 – 6 September 2019</td>
</tr>
<tr>
<td><strong>Study period</strong></td>
<td>9 September – 13 December 2019</td>
</tr>
<tr>
<td><strong>Grace period to fulfil course requirements</strong></td>
<td>16 – 20 December 2019</td>
</tr>
<tr>
<td><strong>Exam period</strong></td>
<td>2 – 29 January 2020</td>
</tr>
<tr>
<td><strong>Final exam period</strong></td>
<td>2 – 31 January 2020</td>
</tr>
<tr>
<td><strong>Graduation ceremony for international students</strong></td>
<td>TBD (early February)</td>
</tr>
</tbody>
</table>

### Days off (no classes, tests or exams):

<table>
<thead>
<tr>
<th>Event</th>
<th>Dates</th>
</tr>
</thead>
<tbody>
<tr>
<td>University Sports Day</td>
<td>12 September 2019 (Thu)</td>
</tr>
<tr>
<td>Faculty Days (Schönherz Cup)</td>
<td>30 September – 1 October 2019 (Mon-Tue)</td>
</tr>
<tr>
<td>National holiday (commemoration of the Revolution of 1956)</td>
<td>23 October 2019 (Wed)</td>
</tr>
<tr>
<td>National holiday (All Saints’ Day)</td>
<td>1 November 2019 (Fri)</td>
</tr>
<tr>
<td>Students’ Scientific Conference (“TDK”)</td>
<td>12 November 2019 (Tue)</td>
</tr>
<tr>
<td>Open Day for secondary-school students</td>
<td>29 November 2019 (Fri)</td>
</tr>
<tr>
<td>Winter holidays (incl. Christmas &amp; New Year’s Eve)</td>
<td>23 December 2019 – 1 January 2020</td>
</tr>
</tbody>
</table>

### Make-up Saturdays (for national, non-holiday days off):

<table>
<thead>
<tr>
<th>Event</th>
<th>Dates</th>
</tr>
</thead>
<tbody>
<tr>
<td>regular classes with even-week Friday schedule</td>
<td>7 December 2019</td>
</tr>
<tr>
<td>extra day of grace period (for in-class test retakes)</td>
<td>14 December 2019</td>
</tr>
</tbody>
</table>
## 2019/2020 1ST SEMESTER

### Electrical Engineering BSc 1st Semester

<table>
<thead>
<tr>
<th>Day</th>
<th>Time</th>
<th>Course</th>
<th>Room</th>
<th>Instructor</th>
</tr>
</thead>
<tbody>
<tr>
<td>Monday</td>
<td>08:15-10:00</td>
<td>Theodor</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Monday</td>
<td>10:15-12:00</td>
<td>Calculus 1 for Informatics</td>
<td>E01</td>
<td>Nagy Ilona, Stubnya</td>
</tr>
<tr>
<td>Monday</td>
<td>12:15-14:00</td>
<td>Digital Design</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Monday</td>
<td>14:15-16:00</td>
<td>Physics 1i</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>08:15-10:00</td>
<td>Calculus 1 for Informatics</td>
<td>E02</td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>10:15-12:00</td>
<td>Digital Design</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>13:15-14:00</td>
<td>Digital Design</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>15:15-16:00</td>
<td>Physics 1i</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>15:15-16:00</td>
<td>Midterm Test</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Wednesday</td>
<td>08:15-10:00</td>
<td>Basics of Programming 1</td>
<td></td>
<td></td>
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<tr>
<td>Wednesday</td>
<td>08:15-10:00</td>
<td>Basics of Programming 1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Wednesday</td>
<td>12:15-14:00</td>
<td>Calculus 1 for Informatics</td>
<td>E03</td>
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</table>

### Electrical Engineering BSc 3rd Semester

<table>
<thead>
<tr>
<th>Day</th>
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<th>Course</th>
<th>Room</th>
<th>Instructor</th>
</tr>
</thead>
<tbody>
<tr>
<td>Monday</td>
<td>08:15-10:00</td>
<td>Theodor</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Monday</td>
<td>10:15-12:00</td>
<td>Calculus 1 for Informatics</td>
<td>E01</td>
<td></td>
</tr>
<tr>
<td>Monday</td>
<td>12:15-14:00</td>
<td>Digital Design</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Monday</td>
<td>14:15-16:00</td>
<td>Physics 1i</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tuesday</td>
<td>08:15-10:00</td>
<td>Calculus 1 for Informatics</td>
<td>E02</td>
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</tr>
<tr>
<td>Tuesday</td>
<td>10:15-12:00</td>
<td>Digital Design</td>
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<td>Calculus 1 for Informatics</td>
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### Electrical Engineering BSc 5th Semester Informatics

<table>
<thead>
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<th>Room</th>
<th>Instructor</th>
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<tbody>
<tr>
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<tr>
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<td>Calculus 1 for Informatics</td>
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</tr>
<tr>
<td>Monday</td>
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<td>Digital Design</td>
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<td>12:15-14:00</td>
<td>Calculus 1 for Informatics</td>
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</table>

### Chemical Engineering

<table>
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<tr>
<th>Day</th>
<th>Time</th>
<th>Course</th>
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</tr>
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<tbody>
<tr>
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<tr>
<td>Monday</td>
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<tr>
<td>Tuesday</td>
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<td>Digital Design</td>
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<tr>
<td>Tuesday</td>
<td>13:15-14:00</td>
<td>Digital Design</td>
<td></td>
<td></td>
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<tr>
<td>Tuesday</td>
<td>15:15-16:00</td>
<td>Physics 1i</td>
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<tr>
<td>Tuesday</td>
<td>15:15-16:00</td>
<td>Midterm Test</td>
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<tr>
<td>Wednesday</td>
<td>08:15-10:00</td>
<td>Basics of Programming 1</td>
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<tr>
<td>Wednesday</td>
<td>08:15-10:00</td>
<td>Basics of Programming 1</td>
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<tr>
<td>Wednesday</td>
<td>12:15-14:00</td>
<td>Calculus 1 for Informatics</td>
<td>E03</td>
<td></td>
</tr>
</tbody>
</table>

### Notes

- These documents are for information purposes only!
- BME Faculty of Electrical Engineering and Informatics
Title
Lecturers
Pre-requisites
Objectives
Synopsis
Assessment
Recaps
...
Your life is changed
Be careful

- New city, new country, new classmates, without family...
- You have to manage your life (accommodation, residence permit, offices, living cost, ...)
- Secondary school => university
  - Difficult subject, hard assessments
  - There is no continuous monitoring
  - misunderstood freedom

  If you do not start learning at the beginning
  ⇒ You will have too much tasks at the end of the semester
  ⇒ You can not fulfil the subjects
  ⇒ You can not register for further subjects in the next semester (because of the pre-requisites)
  ⇒ ...
Focus on your study!

- There is no way to grant exemptions from the
  - pre-requisite rules
  - conditions for admission to the BSc specializations
  - conditions for admission to thesis defense session

- The number of credit points to be accumulated, the grade point average to achieve, the number of recaps are controlled. Failing to satisfy those rules, you are dismissed from your studies.
  - twice the programme duration
  - min. 20 credits / the latest 3 active semesters
  - BSc: cumulated GPA of min. 2.25 at the end of the 4th active semester
  - max. 6 exams/subject

- Plagiarism and cheating: zero tolerance!
  - At least fail of the subject in the given semester (both copying and copied students)
Tuition fee

- If you have no scholarship...

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<thead>
<tr>
<th></th>
<th>For non-EU citizens</th>
<th>For EU citizens</th>
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</thead>
<tbody>
<tr>
<td>BSc program</td>
<td>3200EUR/semester</td>
<td>2250EUR/semester</td>
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</tbody>
</table>

- Stipendium Hungaricum Scholarship
  - max. 2 semesters extension
  - min. 36 credits / the latest 2 active semesters (decision of the Tempus foundation)

- Delay in the study program
  - Time, money, etc.
  - Residence permit...
Student life ↔ Study
Thank you for your attention!